REPLACEMENT SHEET
APPLN. FILING DATE: MAY 26, 2006
TITLE: THREE-PHASE PWM-SIGNAL GENERATING

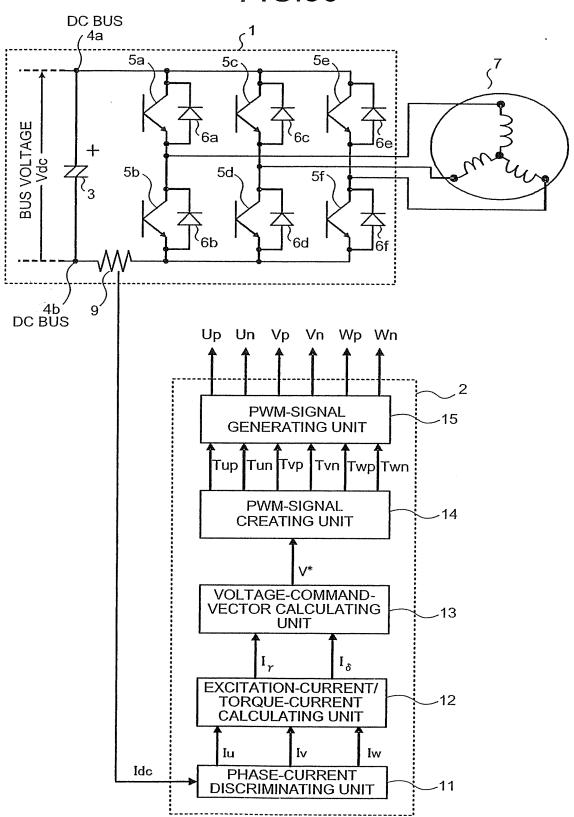
APPARATUS
INVENTOR(S): KOICHI ARISAWA ET AL.
APPLN. NO.: 10/580,849

SHEET 1 OF 4

22/25

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FIG.36



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NVENTOR(2): 172

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SHEET 2 OF 4

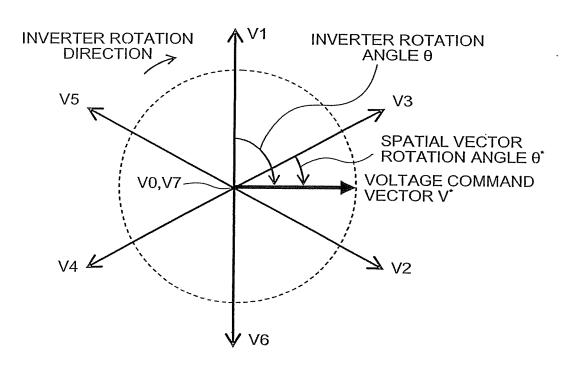
23/25

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FIG.37

BASIC VOLTAGE VECTOR	Wp (W-PHASE POSITIVE SIDE SWITCHING ELEMENT LOGICAL STATE)	Vp (V-PHASE POSITIVE SIDE SWITCHING ELEMENT LOGICAL STATE)	Up (U-PHASE POSITIVE SIDE SWITCHING ELEMENT LOGICAL STATE)	OBSERVABLE PHASE CURRENT
V0	0	0	0	OBSERVATION IMPOSSIBLE
V1	0	0	1	lu
V2	0	1	0	lv
V3	0	1	1	-lw
V4	1	0	0	lw
V5	1	0	1	-lv
V6	1	1	0	-lu
V7	1	1	1	OBSERVATION IMPOSSIBLE

FIG.38 CONVENTIONAL

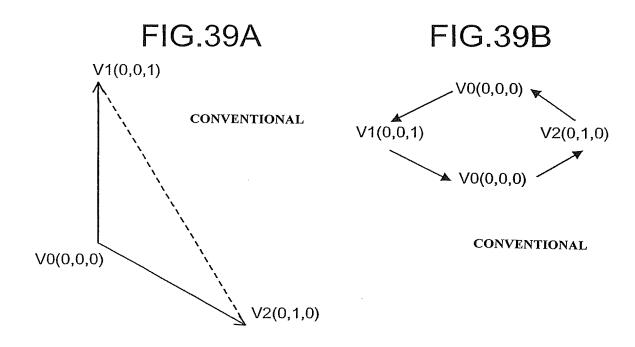


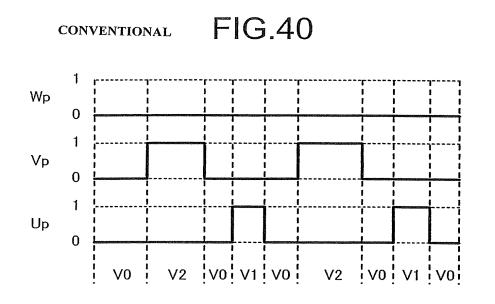
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APPLRATUS
INVENTOR(2): 17

INVENTOR(S): Koichi Arisawa et al. APPLN. No.: 10/580,849

SHEET 3 OF 4

24/25





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ANYENTOR(2)

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SHEET 4 OF 4

25/25

